

ABSTRACT OF THE DISCLOSURE

One embodiment of the present invention includes a control circuit, an increment register, and an accumulator. The control circuit generates a channel enable signal based on control information from a processor at a first clock signal having a first clock frequency. The channel enable signal selects a channel for a satellite in a global positioning system (GPS). The channel operates at a coarse/acquisition (C/A) clock signal having a second clock frequency. The increment register stores an increment value for the selected channel at the first clock signal. The accumulator generates a pseudo-random noise (PN) clock signal to a PN generator using the increment value.